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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Takuji MAEDA et al.

Art Unit:

Application No.: filed concurrently Examiner: Title: SEMICONDUCTOR MEMORY DEVICE AND ITS CONTROL METHOD

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Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Sir:

Prior to the examination for this application, please amend the application as follows:

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Drawings begin on page 4 of this paper.

Remarks begin on page 5 of this paper.